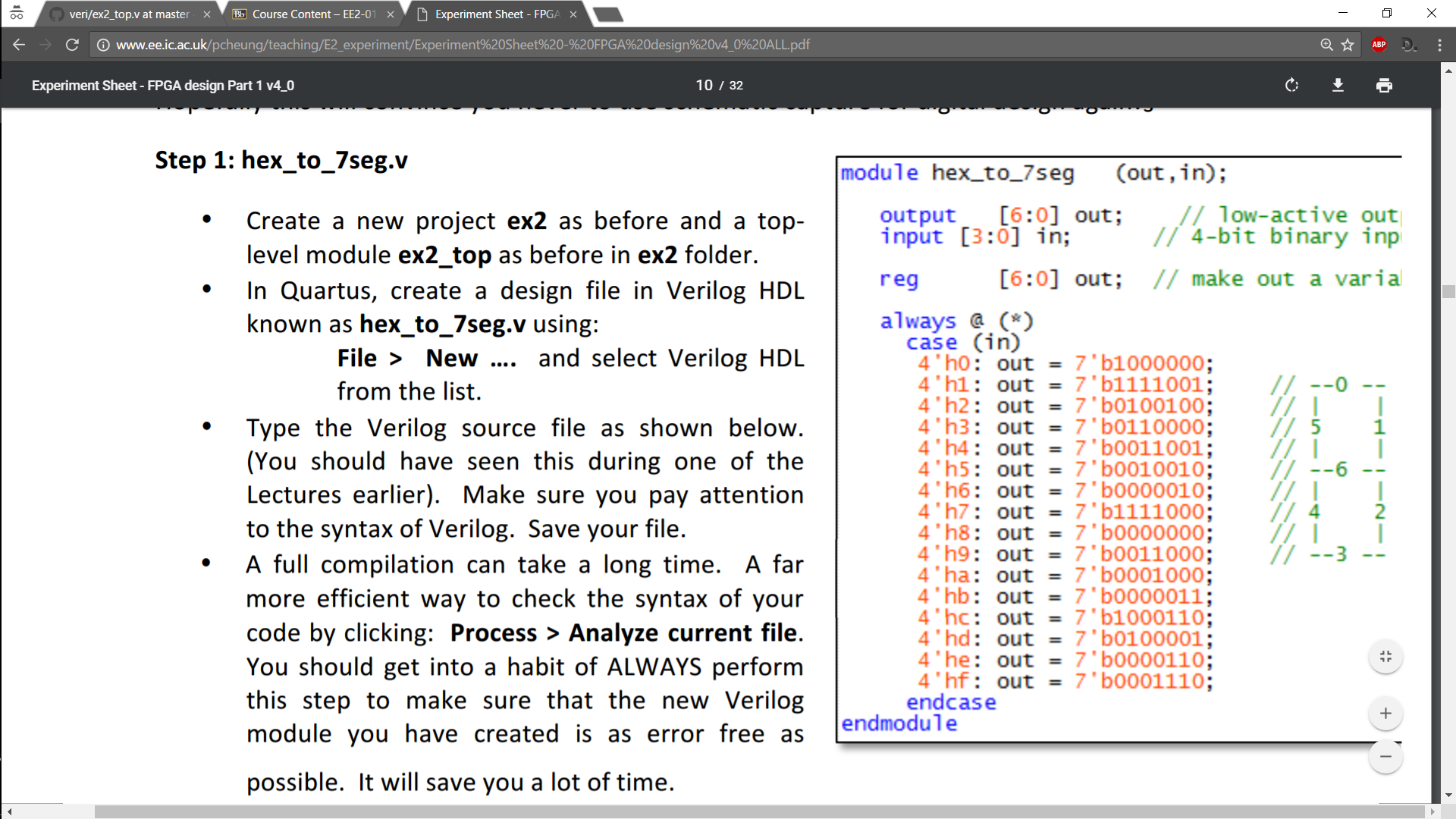
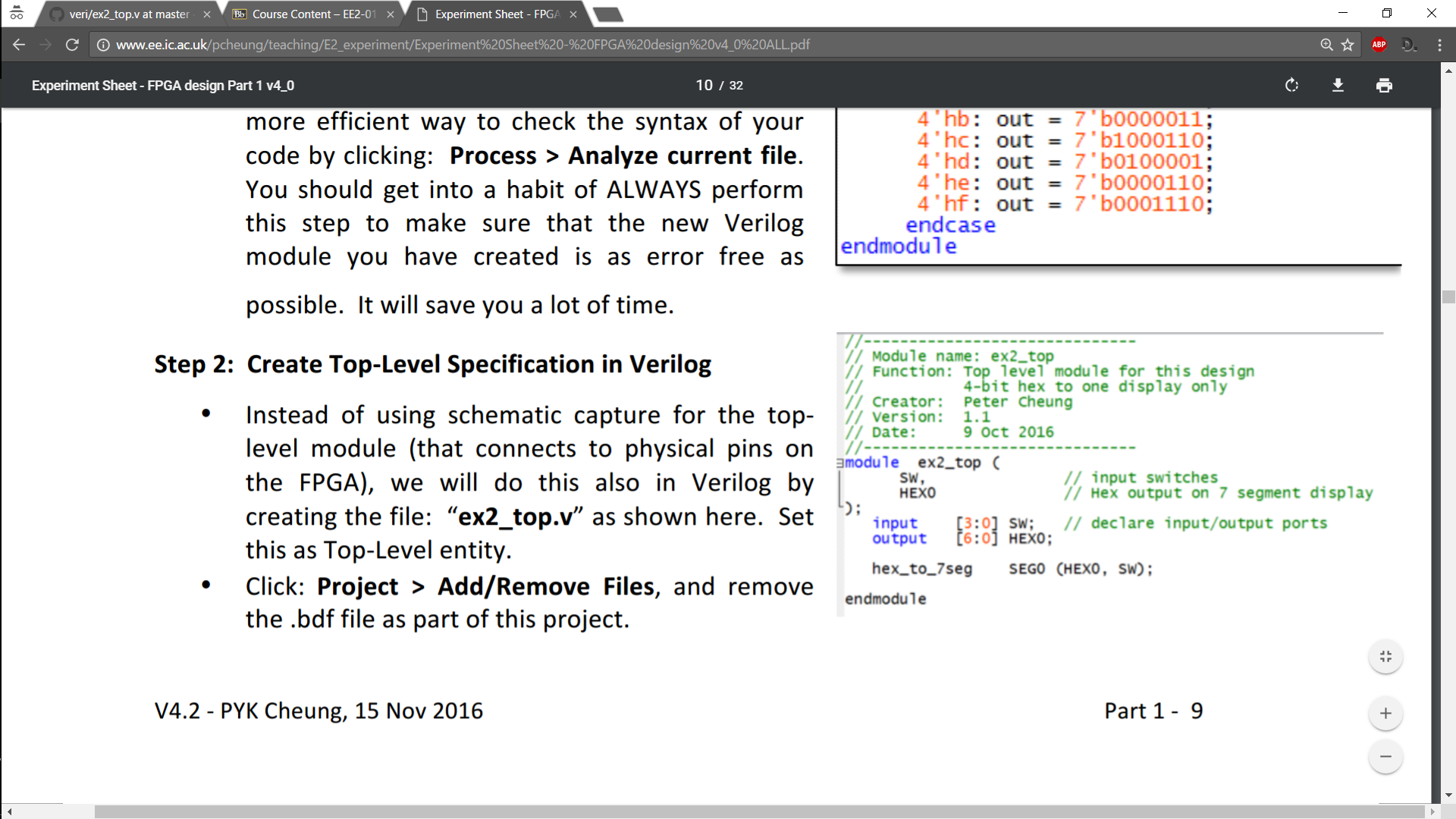
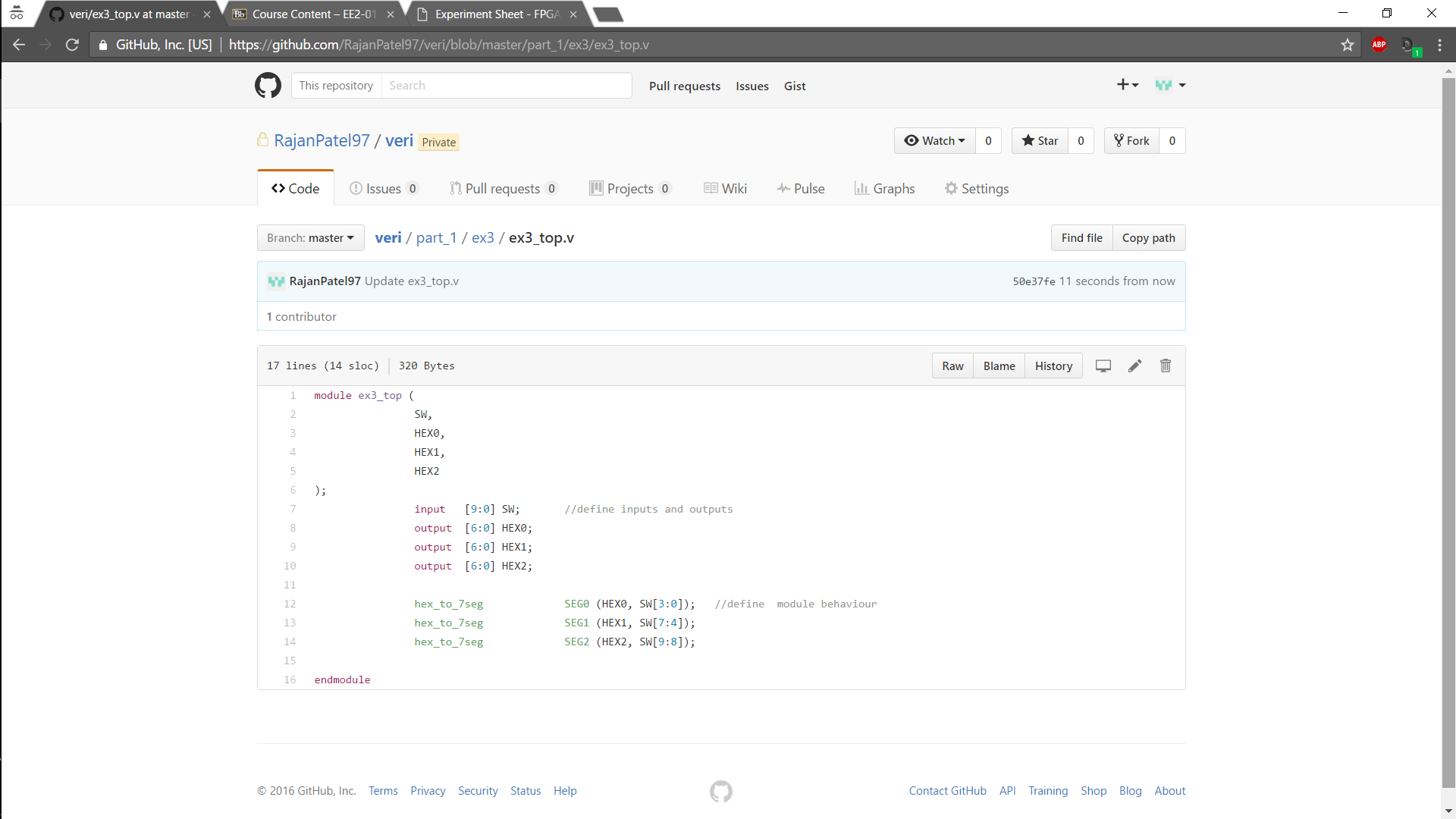
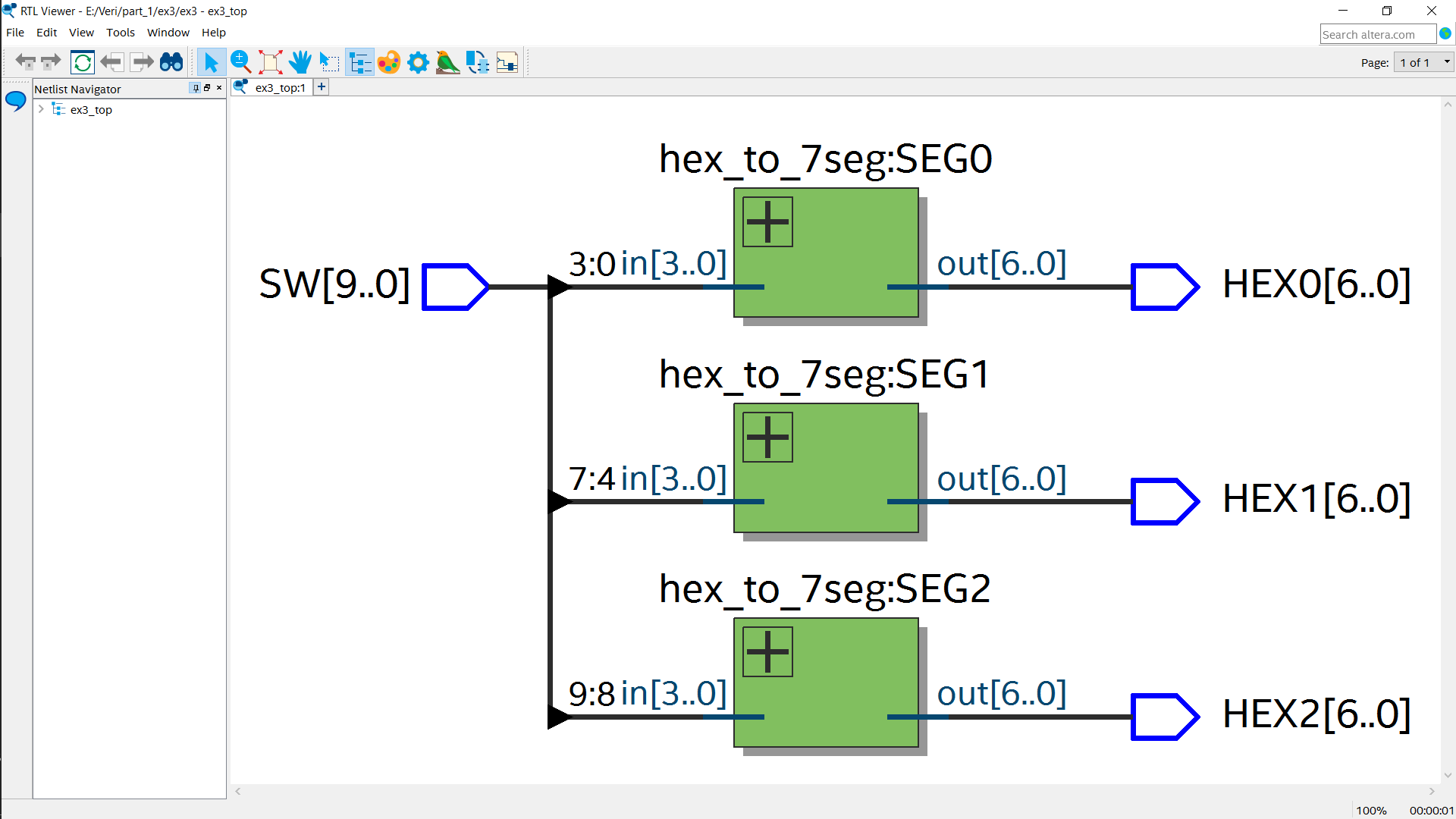
Experiment 2 – 7-Segemnt Decoder in Verilog





Experiment 3 – 10-bit Binary switch





Experiment 4 (Optional) – Displaying 10-bit Binary as BCD digits on the 7-Segment Displays

